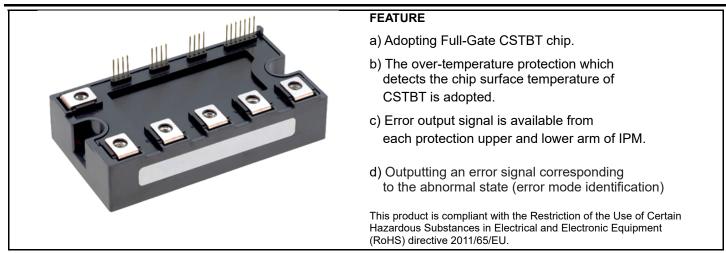


<Intelligent Power Modules>

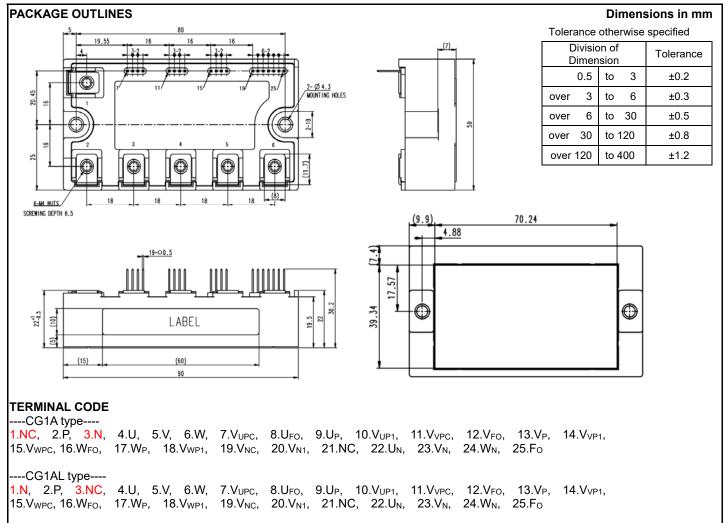
# PM25CG1A120/PM25CG1AL120

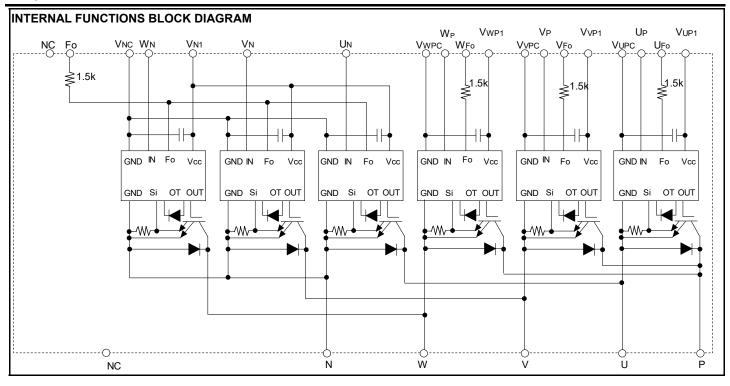
FLAT-BASE TYPE INSULATED PACKAGE



## APPLICATION

General purpose inverter, servo drives and other motor controls





**MAXIMUM RATINGS** (Tvj = 25°C, unless otherwise noted)

#### **INVERTER PART**

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>CES</sub>	Collector-Emitter Voltage	V <sub>D</sub> =15 V, V <sub>CIN</sub> =15 V	1200	V
I <sub>C</sub>		T <sub>c</sub> =25 °C	25	^
I <sub>CRM</sub>	Collector Current	Pulse	50	A
Ptot	Total Power Dissipation	T <sub>c</sub> =25 °C	260	W
l <sub>E</sub>	Emitter Current	T <sub>c</sub> =25 °C	25	^
I <sub>ERM</sub>	(Free-wheeling Diode Forward current)	Pulse	50	A
Tvj	Junction Temperature		-20 ~ +150	°C

\*: Tc measurement point is just under the chip.

#### CONTROL PART

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>D</sub>	Supply Voltage	Applied between: $V_{UP1}$ - $V_{UPC}$ , $V_{VP1}$ - $V_{VPC}$ , $V_{WP1}$ - $V_{WPC}$ , $V_{N1}$ - $V_{NC}$	20	V
V <sub>CIN</sub>	Input Voltage	Applied between: $U_P-V_{UPC}$ , $V_P-V_{VPC}$ , $W_P-V_{WPC}$ , $U_N$ , $V_N$ , $W_N-V_{NC}$	20	V
V <sub>FO</sub>	Fault Output Supply Voltage	Applied between: $U_{FO}$ - $V_{UPC}$ , $V_{FO}$ - $V_{VPC}$ , $W_{FO}$ - $V_{WPC}$ , FO- $V_{NC}$	20	V
I <sub>FO</sub>	Fault Output Current	Sink current at $U_{FO}$ , $V_{FO}$ , $W_{FO}$ , Fo terminals	20	mA

#### TOTAL SYSTEM

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>CC(PROT)</sub>	Supply Voltage Protected by SC	V <sub>D</sub> =13.5 V∼16.5 V, Inverter Part, Tvj=+125°C start	800	V
T <sub>stg</sub>	Storage Temperature	-	-40 ~ +125	°C
Tc	Operating Case Temperature	-	-20 ~ +125	°C
Visol	Isolation Voltage	60Hz, Sinusoidal, Charged part to Base plate, AC 1min, RMS	2500	V

\*: Tc measurement point is just under the chip.

## THERMAL RESISTANCE

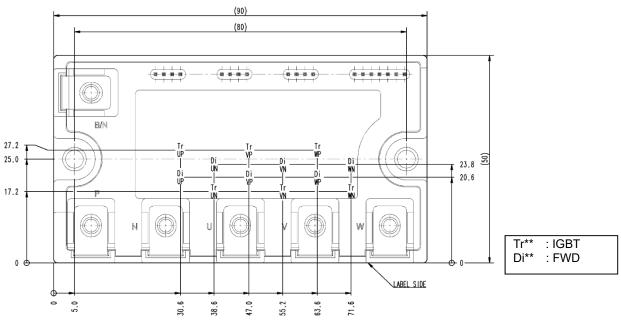
Symbol	Parameter	Conditions	Limits			Linit
		Conditions		Тур.	Max.	Unit
R <sub>th(j-c)Q</sub>	Thermal Resistance	Junction to case, IGBT, per 1 element (Note1)	-	-	0.48	K/W
$R_{th(j-c)D}$		Junction to case, FWD, per 1 element (Note1)	-	-	0.78	
$R_{th(c-s)}$	Contact Thermal Resistance	Case to heat sink, per 1 module,	10.1	19.1	-	K/kW
		Thermal grease applied (Note.1, 2)	-	13.1		

Note1. If you use this value,  $R_{\text{th}(s\text{-}a)}$  should be measured just under the chips.

Note2. Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9W/(m·K), D<sub>(C-S)</sub>=50 µm.

#### CHIP LOCATION (Top view)

Dimension in mm, torelance: ±1mm



**ELECTRICAL CHARACTERISTICS** (Tvj= 25°C, unless otherwise noted) **INVERTER PART** 

Symbol	Doromotor	Conditions		Limits			Unit	
Symbol	Parameter	Conditions			Min.	Тур.	Max.	Unit
		V <sub>D</sub> =15 V, I <sub>C</sub> =25 A Tvj=25 °C	T: - 05 °C	Terminal	-	-	1.7	
V	Collector-Emitter Saturation Voltage		TVJ=25 °C	Chip	-	1.3	-	v
V <sub>CEsat</sub>	_	(-0)/Dulad (Fig. 1)	Tvj=125 °C	Terminal	-	-	1.95	v
		V <sub>CIN</sub> =0 V, Pulsed, (Fig.1)	1 VJ-125 C	Chip	-	1.5	-	
	Emitter-Collector Voltage	V <sub>D</sub> =15 V, I <sub>E</sub> =25 A, Tvj=25	Tui-25 °C	Terminal	-	-	2.35	
V <sub>EC</sub>			TVJ-25 C	Chip	-	1.75	-	- V
VEC		V <sub>CIN</sub> = 15 V, pulsed, (Fig.2) Tvj=125 °C	Tui-125 °C	Terminal	-	-	2.6	
			TVJ-125 C	Chip	-	1.95	-	
t <sub>on</sub>		V <sub>D</sub> =15 V, V <sub>CIN</sub> =0 V↔15 V, V <sub>CC</sub> =600 V, I <sub>C</sub> =25A, Tvj=125 °C, Inductive Load			0.3	0.7	1.2	
t <sub>rr</sub>				-	0.13	0.4		
t <sub>c(on)</sub>	Switching Time				-	0.2	0.4	μs
t <sub>off</sub>					-	1.0	2.8	
t <sub>c(off)</sub>		(Fig.3, 4)		-	0.4	1.2		
	Callastan Ensittan Out off Ourset			Tvj=25 °C	-	-	1	
I <sub>CES</sub>	Collector-Emitter Cut-off Current	ector-Emitter Cut-off Current $V_{CE}=V_{CES}, V_D=15 V, V_{CIN}=15 V $ (Fig.5)		Tvj=125 °C	-	-	10	mA

INSULATED TYPE

#### ELECTRICAL CHARACTERISTICS (Tvj = 25°C, unless otherwise noted)

#### CONTROL PART

Symbol	Parameter Conditions				Limits		Unit
Symbol	Parameter	Conditions		Min.	Тур.	Max.	
			V <sub>P1</sub> -V <sub>PC</sub>	-	4	6	
		V <sub>D</sub> =15 V, V <sub>CIN</sub> =15 V	V <sub>N1</sub> -V <sub>NC</sub>	-	12	18	
ID	Circuit Current	$V_D$ =15 V, $V_{CIN}$ =0 V $\leftrightarrow$ 15 V, $V_{CC}$ =800 V	V <sub>P1</sub> -V <sub>PC</sub>	-	10	12	mA
		I <sub>c</sub> =0A, Tvj=125 °C, f <sub>c</sub> ≤20kHz	V <sub>N1</sub> -V <sub>NC</sub>	-	30	36	]
$V_{th(ON)}$	Input ON Threshold Voltage	Applied between: U <sub>P</sub> -V <sub>UPC</sub> , V <sub>P</sub> -V <sub>VPC</sub> , W <sub>P</sub> -V <sub>WPC</sub> , U <sub>N</sub> , V <sub>N</sub> , W <sub>N</sub> -V <sub>NC</sub>		1.2	1.5	1.8	v
$V_{th(OFF)}$	Input OFF Threshold Voltage			1.7	2.0	2.3	
SC	Short Circuit Trip Level	-20≤Tvj≤125 °C, V <sub>D</sub> =15 V (Fig.3, 6)		50	-	-	А
t <sub>d(SC)</sub>	Short Circuit Current Delay Time	V <sub>D</sub> =15 V, Tvj=125 °C (Fig.3, 6)		-	2.0	-	μs
ОТ		ver Temperature Protection Detect temperature of IGBT chip surface	Trip level	150	-	-	<b>.</b>
OT <sub>(hys)</sub>	Over temperature Protection		Hysteresis	-	20	-	
UVt	Supply Circuit		Trip level	11.0	12.0	12.7	v
UVr	Under-Voltage Protection	-	Reset level	-	12.5	-	
I <sub>FO(H)</sub>	Foult Output Ourrent	$\gamma = 15 \gamma \gamma = 15 \gamma \gamma $ (Note2)		-	-	0.01	mA
I <sub>FO(L)</sub>	Fault Output Current	but Current $V_D = 15 V, V_{FO} = 15 V$ (Note3)		-	10	15	- mA
			ОТ	-	8.0	-	
t <sub>FO</sub>	Fault Output Pulse Width	V <sub>D</sub> =15 V (Note3)	UV	-	4.0	-	ms
			SC	-	2.0	-	

Note3. Fault output is given only when the internal SC, OT & UV protections schemes of either upper or lower arm device operate to protect it.

#### MECHANICAL RATINGS AND CHARACTERISTICS

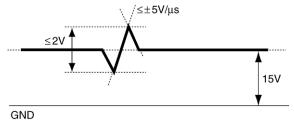
Symbol	Parameter	Conditions		Limits		
				Тур.	Max.	Unit
Ms	Mounting Torque	Mounting part screw : M4	1.5	1.7	2.0	N•m
Mt	Mounting Torque	Main terminal part screw : M4	1.5	1.7	2.0	INTII
m	mass	-	-	175	-	g

#### **RECOMMENDED CONDITIONS FOR USE**

Symbol	Parameter	Conditions	Recommended value	Unit
V <sub>cc</sub>	Supply Voltage	Applied across P-N terminals	≤ 800	V
V <sub>D</sub>	Control Supply Voltage	Applied between : VUP1-VUPC, VVP1-VVPC, VWP1-VWPC, VN1-VNC (Note4)	15.0±1.5	V
V <sub>CIN(ON)</sub>	Input ON Voltage	Applied between :	≤ 0.8	v
V <sub>CIN(OFF)</sub>	Input OFF Voltage	$U_{P}\text{-}V_{UPC}, V_{P}\text{-}V_{VPC}, W_{P}\text{-}V_{WPC}, U_{N}, V_{N}, W_{N}\text{-}V_{NC}$	≥ 9.0	v
f <sub>PWM</sub>	PWM Input Frequency	Using Application Circuit of Fig. 8	≤ 20	kHz
t <sub>dead</sub>	Arm Shoot-through Blocking Time	For IPM's each input signals (Fig.7)	≥ 2.5	μs

This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note4. With ripple satisfying the following conditions: dv/dt swing  $\leq \pm 5 V/\mu s$ , Variation  $\leq 2 V$  peak to peak



INSULATED TYPE

## **PRECAUTIONS FOR TESTING**

VD

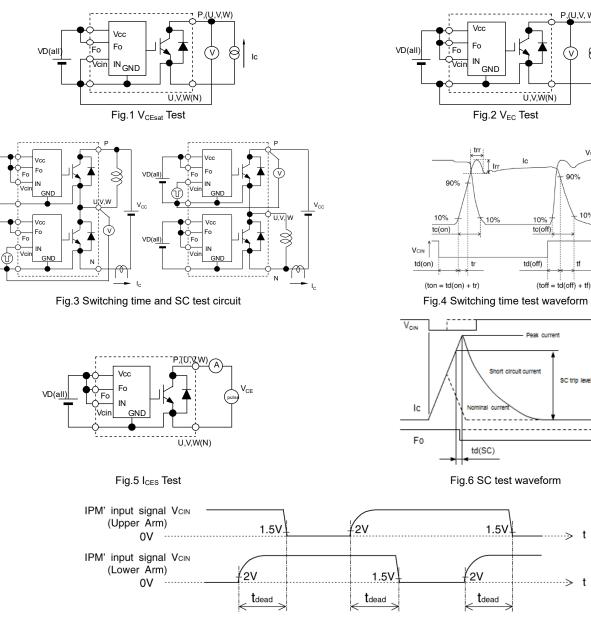
VD(all

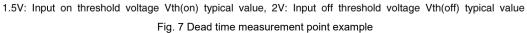
1. Before applying any control supply voltage (V<sub>D</sub>), the input terminals should be pulled up by resistors, etc. to their corresponding supply voltage and each input signal should be kept off state.

After this, the specified ON and OFF level setting for each input signal should be done.

2. When performing "SC" tests, the turn-off surge voltage spike at the corresponding protection operation should not be allowed to rise above VCES rating of the device.

(These test should not be done by using a curve tracer or its equivalent.)





P.(U.V.W)

VCE

10%

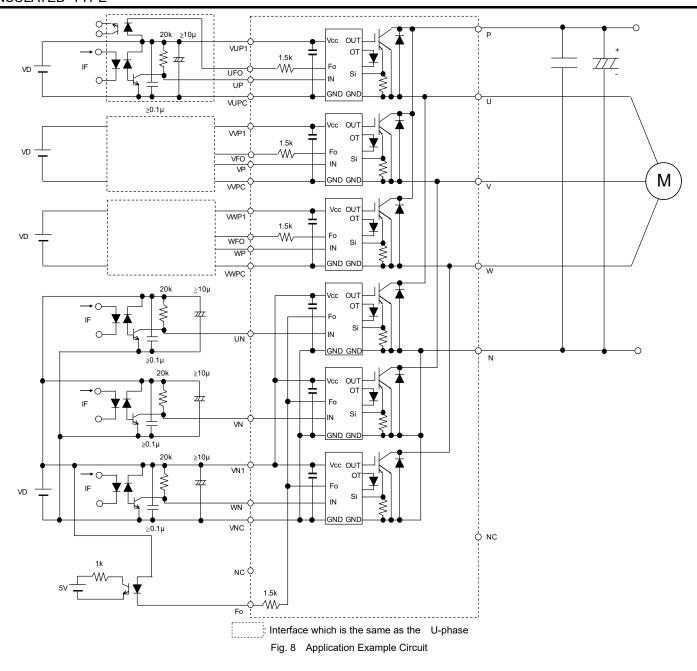
tf

SC trip leve

t

.

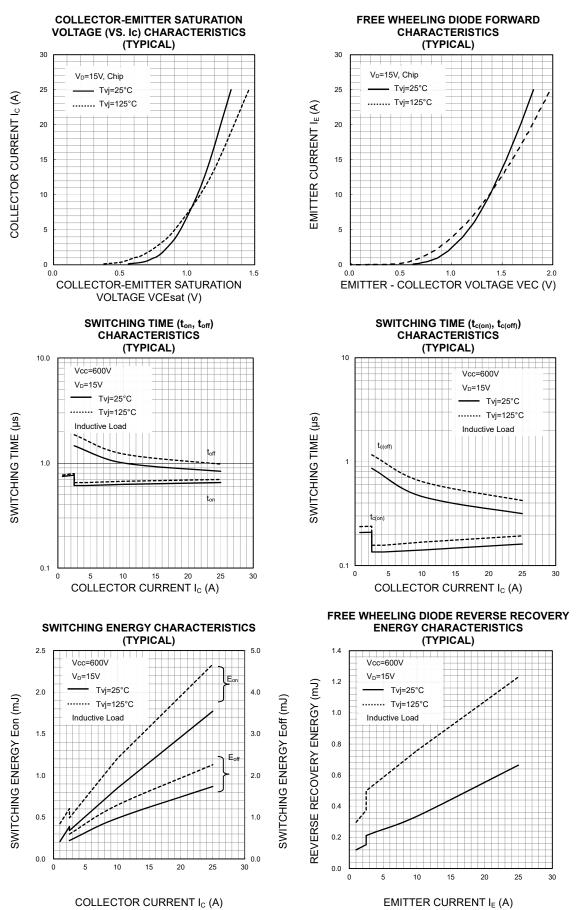
90%

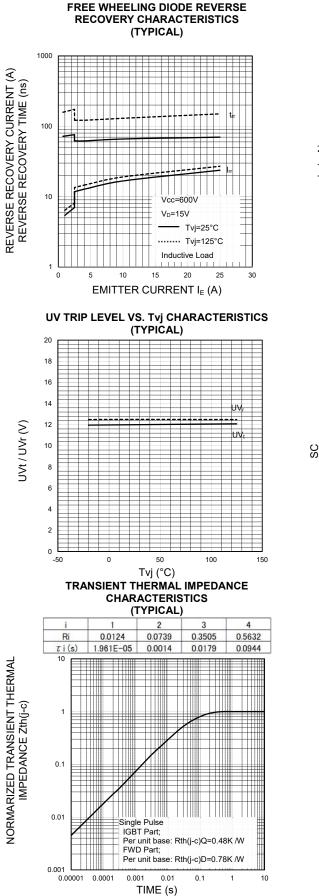


## NOTES FOR STABLE AND SAFE OPERATION ;

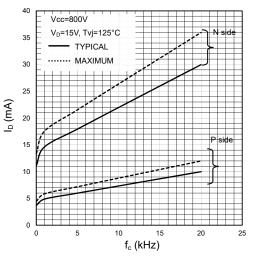
- Design the PCB pattern to minimize wiring length between opto-coupler and IPM's input terminal, and also to minimize the stray capacity between the input and output wirings of opto-coupler.
- Connect low impedance capacitor between the Vcc and GND terminal of each fast switching opto-coupler.
- Fast switching opto-couplers:  $t_{PLH}$ ,  $t_{PHL} \le 0.8 \mu s$ , Use High CMR type.
- Slow switching opto-coupler: CTR > 100%
- Use 4 isolated control power supplies (V<sub>D</sub>). Also, care should be taken to minimize the instantaneous voltage charge of the power supply.
- Make inductance of DC bus line as small as possible, and minimize surge voltage using snubber capacitor between P and N terminal.

## PERFORMANCE CURVES

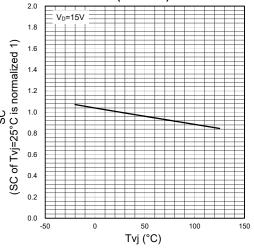




#### I<sub>D</sub> VS. f<sub>C</sub> CHARACTERISTICS (TYPICAL, MAXIMUM)



SC TRIP LEVEL VS. Tvj CHARACTERISTICS (TYPICAL)



## Keep safety first in your circuit designs!

This product is designed for industrial application purpose. The performance, the quality and support level of the product is guaranteed by "Customer's Std. Spec.".

Mitsubishi Electric Corporation puts its reasonable effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them by the reliability lifetime such as Power Cycle, Thermal Cycle or others, or to be used under special circumstances(e.g. high humidity, dusty, salty, highlands, environment with lots of organic matter / corrosive gas / explosive gas, or situation which terminal of semiconductor products is received strong mechanical stress). In the customer's research and development, please evaluate it not only with a single semiconductor product but also in the entire system, and judge whether it's applicable. Furthermore, trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits (e.g. appropriate fuse or circuit breaker between a power supply and semiconductor products), (ii) use of non-flammable material or (iii) prevention against any malfunction or mishap.

## Notes regarding these materials

- •These materials are intended as a reference to assist our customers in the selection of the Mitsubishi semiconductor product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Mitsubishi Electric Corporation or a third party.
- •Mitsubishi Electric Corporation assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, or circuit application examples contained in these materials.
- •All information contained in these materials, including product data, diagrams and charts represents information on products at the time of publication of these materials, and are subject to change by Mitsubishi Electric Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Mitsubishi Electric Corporation or an authorized Mitsubishi Semiconductor product distributor for the latest product information before purchasing a product listed herein.
- The information described here may contain technical inaccuracies or typographical errors. Mitsubishi Electric Corporation assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.
- Please also pay attention to information published by Mitsubishi Electric Corporation by various means, including the Mitsubishi Semiconductor home page (www.MitsubishiElectric.com/semiconductors/).
- •When using any or all of the information contained in these materials, including product data, diagrams, and charts, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Mitsubishi Electric Corporation assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
- •Mitsubishi Electric Corporation semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Therefore, this product should not be used in such applications. Please contact Mitsubishi Electric Corporation or an authorized Mitsubishi Semiconductor product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
- •In the case of new requirement is available, this material will be revised upon consultation.
- •The prior written approval of Mitsubishi Electric Corporation is necessary to reprint or reproduce in whole or in part these materials.
- •If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
- Any diversion or re-export contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.

•Please contact Mitsubishi Electric Corporation or an authorized Mitsubishi Semiconductor product distributor for further details on these materials or the products contained therein.

Generally the listed company name and the brand name are the trademarks or registered trademarks of the respective companies.

Note:

The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

© 2017 MITSUBISHI ELECTRIC CORPORATION. ALL RIGHTS RESERVED.